

# Analysis of IIR Filters by Interval Response

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**Abstract**—The classical theory of signal processing assumes that the designed IIR filters are continuous and have infinitely accurate coefficients. However, when developing filters for real-world digital signal processing tasks, it is necessary to take into account the finite precision of the coefficients representation, especially considering the fixed-point arithmetic. In this paper, we propose a new approach, which we call the interval size approach, making it easy to evaluate the actual digital filter response for various fixed-point arithmetic parameters. We provide illustrative examples to demonstrate the frequency response bounds evolution as an order, cutoff frequency, and signal frequency are varying. We show that the interval size can be used as a well-suited accuracy factor of a digital filter.

**Keywords**—signal processing, digital filters, quantization, interval size

## I. INTRODUCTION

A digital filter amplifies or reduces certain parameters of a discrete-time signal using mathematical operations [1]. It is widely used in signal processing, and its extraordinary performance is one of the key reasons that DSP has become so popular [2]. Digital filters are categorized as finite duration pulse response (FIR) filters with nonzero pulse response for only a finite number of samples, and infinite duration pulse response (IIR) with an infinite number of nonzero samples in an impulse response [3].

When designing high-performance signal processing systems, a priori it is considered that the filter response is acceptable. Nevertheless, in hardware systems with fixed-point arithmetic, i.e. microcontrollers, digital signal processors and FPGA, or in systems with highly demanding performance specifications, the importance of filter coefficient accuracy increases. With a lack of accuracy, the signal specifications can be completely missed, and the signal – distorted [4–6]. Therefore, a common goal in finite precision analysis is to choose a word length such that the digital system provides a sufficiently accurate realization of the desired frequency response while minimizing the complexity and cost of hardware and software [7].

In digital signal processing, the effects of finite word length are known as one of the most significant factors. Mullis [8] and Huang [9] demonstrated the influence of quantization errors on digital filter performance depends on the performance chosen for the filter implementation. Rader and Gold [10] showed that small errors in the denominator or numerator coefficients may cause large poles or zeros offsets. Moreover, Goodall et al. [11–12] introduced the concept of coefficients sensitivity to word lengths.

Today there are some common approaches to handling the inaccuracies in parameter values, e.g. to develop design procedures that are inherently insensitive to parameter inaccuracies, or to choose specifications that are consistent with the limited register length [13]. Another possibility is the implementation of interval sets. In [14] it was shown that the frequency response of interval FIR and IIR filters may be guaranteed to lie within specified bounds on corresponding small subsets of extreme filters, each of which has fixed coefficients.

Interval mathematics is well-known in the literature. For example, in previous work, we [15] presented a theorem that identifies the reliability of fixed-point calculations. A technique using two extension intervals to reject a fixed-point design in case a required accuracy is not reached is discussed in [16]. However, the analysis of a digital filter frequency response based on the interval arithmetic still requires a study. This paper uses the proposed concept to evaluate the response of the Butterworth filter.

The paper is organized as follows: In section II, we define the domain and basics of the proposed methodology. In section III, we describe the results. Conclusion and discussion are given in Section IV.

## II. PRELIMINARY CONCEPTS AND METHODOLOGY

### A. IIR filter

IIR digital filters are characterized by infinite impulse response. They have output feedback, which allows achieving a more selective frequency response with a lower number of coefficients. However, IIR digital filters are sensitive to polynomial coefficients varying. IIR digital filters are described by the transfer function [17]:

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$$H(z) = \frac{\sum_{l=0}^N a_l y[n-l]}{\sum_{k=0}^M b_k x[n-k]}, \quad (1)$$

where  $N$  – zeros coefficients,  $M$  – poles coefficients,  $b_k$  and  $a_l$  – filter coefficients. More general description of (1) with  $a_0 = 1$ :

$$y(k) = \sum_{k=0}^M b_k x[n-k] - \sum_{l=1}^N a_l y[n-l]. \quad (2)$$

### B. Quantization error

In the process of converting continuous values to discrete values the difference between the original analog signal and its quantized version appears. This error is called a “quantization error”. Thus, quantizing an analog signal corresponds to adding a certain amount of noise. The fewer bits are used in quantization, the more noise is added. The quantization error is given by the formula:

$$\sigma = \frac{A}{2Q}, \quad (3)$$

where  $Q = 2^b$ ,  $b$  is the number of bits,  $A$  is the full range of analog signal. Using (3), the coefficients of (2) can be rewritten as:

$$\begin{aligned} a_k &= a_k - \frac{Q}{2} & a_k &= a_k + \frac{Q}{2} \\ b_k &= b_k - \frac{Q}{2} & b_k &= b_k + \frac{Q}{2} \end{aligned} \quad (4)$$

### C. Simulation

We propose the following simulation algorithm:

**Step 1:** Generate the poles and zeros of the transfer function (1).

**Step 2:** Choose a number of bits and calculate quantization error, (3).

**Step 3:** Insert the quantization error into the poles and zeros. Then, (1) can be rewritten in the following combinations:

$$y_1[k] = \sum_{l=1}^N a_l y[n-l] + \sum_{k=0}^M b_x x[n-k], \quad (5)$$

$$y_2[k] = \sum_{l=1}^N a_l y[n-l] + \sum_{k=0}^M b_x x[n-k], \quad (6)$$

$$y_3[k] = \sum_{l=1}^N a_l y[n-l] + \sum_{k=0}^M b_x x[n-k], \quad (7)$$

$$y_4[k] = \sum_{l=1}^N a_l y[n-l] + \sum_{k=0}^M b_x x[n-k]. \quad (8)$$

**Step 4:** Filter signal using filters (5)-(8).

**Step 5:** Get the maximum and minimum values in each iteration:

$$y_{\max}[k] = \max(y_1, y_2, y_3, y_4), \quad (9)$$

$$y_{\min}[k] = \min(y_1, y_2, y_3, y_4), \quad (10)$$

**Step 6:** Calculate the interval size.

$$I = y_{\max}[k] - y_{\min}[k]. \quad (11)$$

The obtained values can be used to analyze any IIR filter. In our study, we focused on Butterworth filter interval errors studying when 16-bit and 32-bit fixed-point arithmetic is used.

## III. TESTS AND RESULTS

To validate the proposed technique, the following tests were performed:

**Test 1:** Alteration of filter order with fixed cutoff frequency 40 Hz and input sine signal of 40 and 60 Hz frequency. The results obtained for 16 and 32 bits are presented in Tables I and II, respectively. The simulation was stopped when the response became unstable. Thus, we found that the highest possible order for 16 bits is 6th, and for 32 bits is 13th.

**Test 2:** Alteration of filter cutoff frequency using input sine signal with 40 and 60 Hz frequency and 5th order for 16 bits and 10th order for 32 bits. The results are shown in Fig. 1-4.

**Test 3:** Alteration of the input sinusoidal signal with filter cutoff frequency at 40 Hz and 5th order for 16 bits and 10th order for 32 bits. The results are shown in Fig. 5-8.

In test 1, the increase in filter order characterizes the increase of the interval size. But in the 32-bit system of 11th order, the size of the range decreased, showing that the level of uncertainty decreased. In test 2, in comparison to the 16-bit high-pass and low-pass filters, the only difference was a larger interval size at the cutoff frequency of 40 Hz. At 32 bits the low-pass filter obtained a smaller interval size. In test 3, the variation of the input signal determines which filter frequencies are least accurate.

Presented results make it possible to determine the best system order, as well as the cutoff frequency and the signal frequency, which provide the lower signal distortion in the fixed-point digital filter.

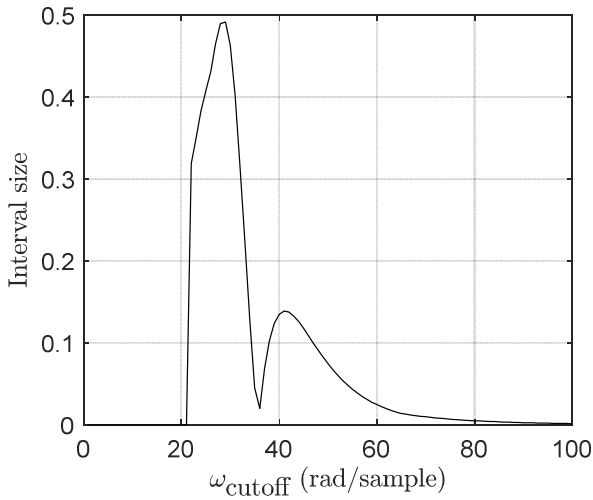


Fig. 1. Test 2: 16-bit low-pass filter of order 5.

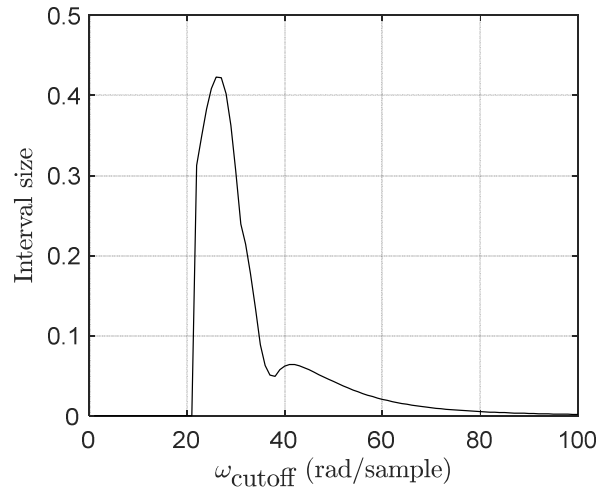


Fig. 2. Test 2: 16-bit high-pass filter of order 5.

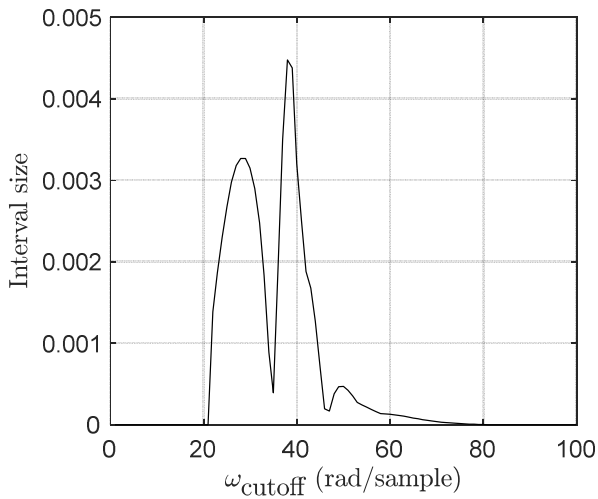


Fig. 3. Test 2: 32-bit low-pass filter of order 10.

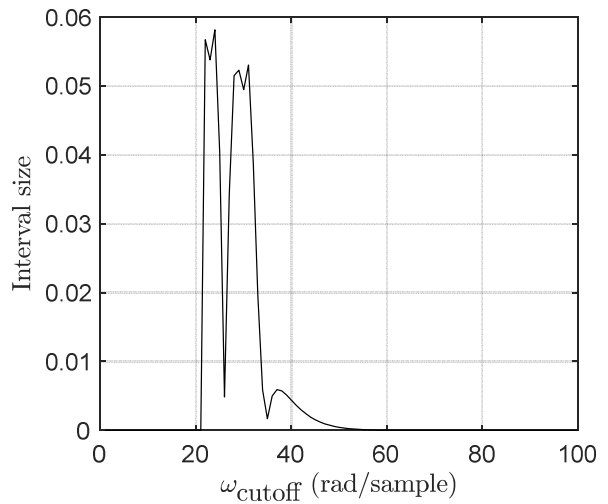


Fig. 4. Test 2: 32-bit high-pass filter of order 10.

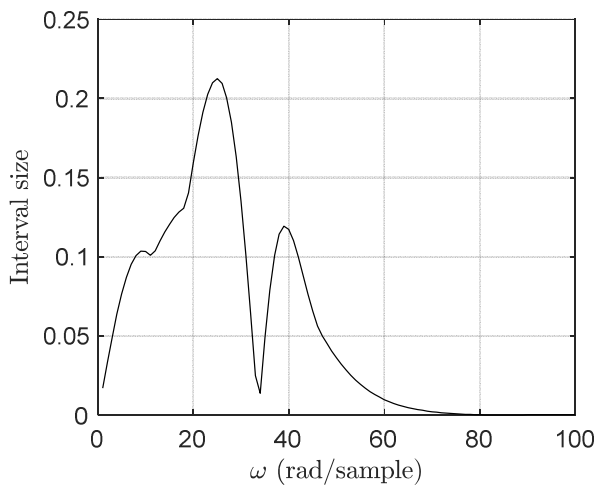


Fig. 5. Test 3: 16-bit low-pass filter of order 5.

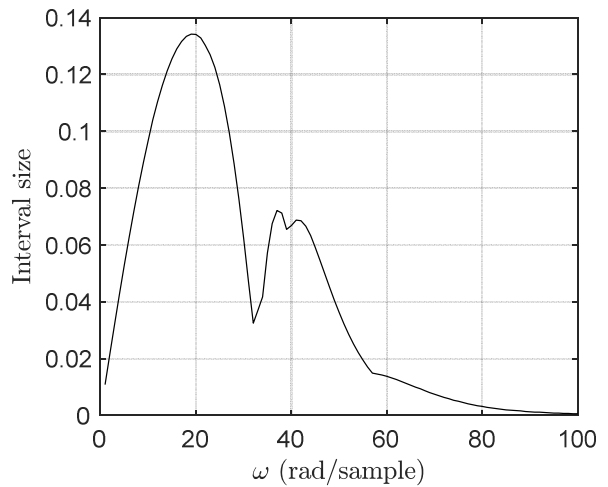


Fig. 6. Test 3: 16-bit high-pass filter of order 5.

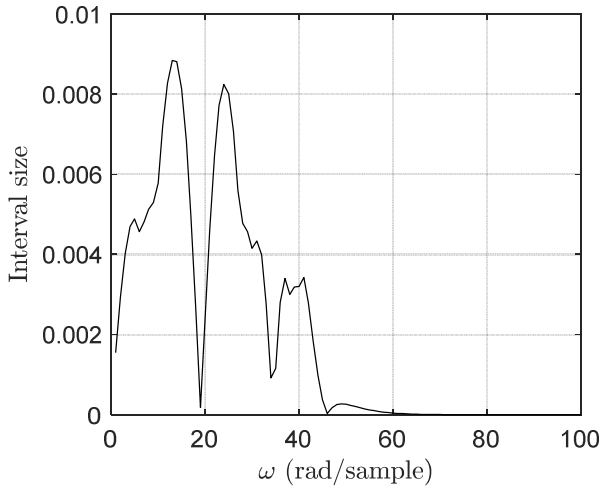


Fig. 7. Test 3: 32-bit low-pass filter of order 10.

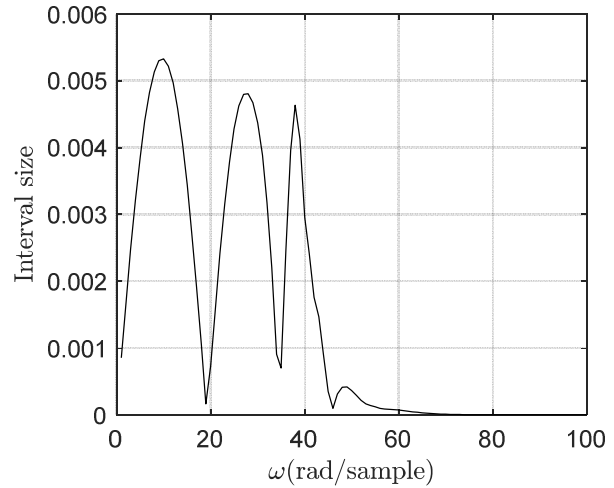


Fig. 8. Test 3: 32-bit high-pass filter of order 10.

TABLE I. INTERVAL SIZE WITH 16 BITS

$n^{th}$ order	Low-pass	High-pass
1	0.00013157	0.00013783
2	0.00112199	0.00112199
3	0.00615655	0.00510382
4	0.02887566	0.01370685
5	0.10441428	0.02955294
6	0.16903701	0.11799196

TABLE II. INTERVAL SIZE WITH 32 BITS

$n^{th}$ order	low-pass	high-pass
1	0.000000020077	0.000000021030
2	0.000000171213	0.000000171213
3	0.000000939418	0.000000778782
4	0.000004406045	0.000002061205
5	0.000015834518	0.000004456805
6	0.000020584737	0.0000016033810
7	0.0000575346152	0.0000181295321
8	0.0003223693824	0.0001619635586
9	0.0009850016594	0.0010024738655
10	0.0043719068664	0.0043719234919
11	0.0175192104908	0.0136181514784
12	0.0080165777634	0.0080168544070
13	0.2815356824937	0.1973608462015

IV. CONCLUSION

An effective method to avoid such unwanted effects of filter coefficients quantization as quantization noise, limit cycles, and overflow oscillations is to simulate the system with various quantization settings and discard solutions that do not meet the criteria of stability and accuracy. To avoid extensive calculations, we propose an interval-based technique, according to which it is possible to determine the boundary values of the filter response accuracy. Therefore, the frequency response of the IIR filters may have characteristics within strictly specified limits, and the interval size can be used as an accuracy factor of a digital filter.

The limitation of this work is that we investigated only one type of filter zeros and poles placement, and also only

two different machine word lengths were considered. In the future, the results will be extended to a wide class of filters.

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